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(54) TRANSMITTING DEVICE AND TRANSMITTING METHOD

(57) A turbo encoding section 102 turbo-encodes transmission data, outputs systematic bit data to a first modulation section 103a and outputs parity bit data to a second modulation section 104. The first modulation section 103a always QPSK-modulates systematic bit data. The second modulation section 103b adaptively modulates parity bit data. The control section 101 out-

puts a control signal for carrying out 16QAM modulation to the second modulation section 103b when the RSSI value is equal to or higher than a threshold and outputs a control signal for carrying out QPSK modulation to the second modulation section 103b when the RSSI signal value is less than the threshold. In this way, it is possible to further make the error rate characteristic compatible with the transmission efficiency.

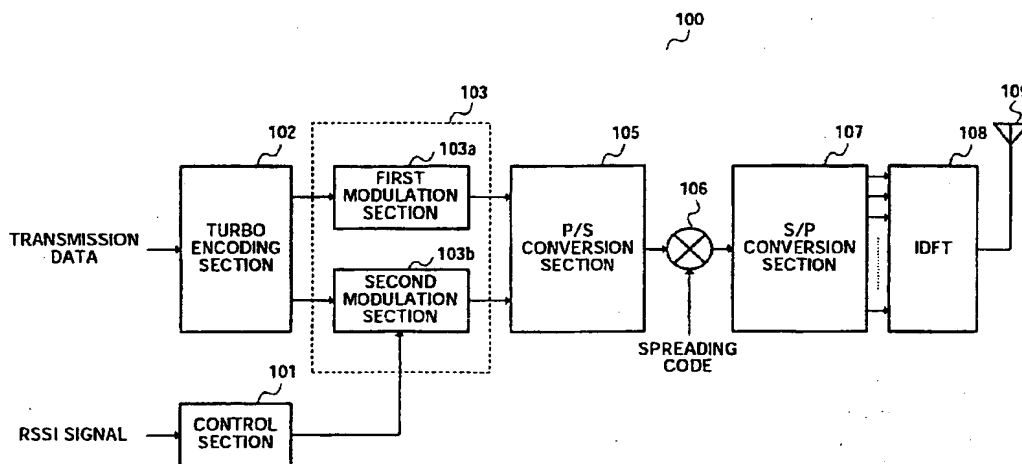


FIG.2

Description

Technical Field

[0001] The present invention relates to a transmission apparatus and transmission method, and more particularly, to a transmission apparatus and transmission method for transmitting transmission data encoded using a turbo encoder.

Background Art

[0002] Conventionally, a turbo code is available as an error correcting/encoding scheme and used by the 3GPP for standardization. Compared to other error correcting schemes, this turbo code is characterized by its ability to obtain an error rate characteristic of extremely high quality.

[0003] An example of a transmission apparatus using a conventional turbo code is shown in FIG.1. The transmission apparatus 10 shown in FIG.1 is provided with a turbo encoding section 11, a parallel/serial (hereinafter referred to as "P/S") conversion section 12, a spreading section 13, a radio transmission section 14 and an antenna 15.

[0004] A turbo encoding section 11 receives a transmission signal, converts the signal to a turbo code, generates systematic bit data and parity bit data, and gives those data to the P/S conversion section 12. The P/S conversion section 12 receives the systematic bit data and parity bit data from the turbo encoding section 11, carries out P/S modulation, generates a serial modulated signal and gives the signal to the spreading section 13.

[0005] The spreading section 13 carries out spreading processing on the modulated signal from the P/S conversion section 12, generated a spread signal and gives the spread signal to the radio transmission section 14. Here, according to the 3GPP (Third Generation Partnership Project) standardization scheme, the spreading section 13 uses the same spreading code for systematic bit data and parity bit data of the modulated signal, and uses the same spreading factor and the same number of spreading codes assigned. The radio transmission section 14 receives the spread signal from the spreading section 13 and sends the signal through the antenna 15.

[0006] Here, as a conventional CDMA transmission apparatus, there is one provided with a section that spreads one user signal with a plurality of spread signals assigned thereto to improve the error rate characteristic.

[0007] Furthermore, the conventional CDMA transmission apparatus is also provided with a data addition section for converting an input data string with variable bits to a data string XA with a predetermined number of bits, a first encoding section for encoding the data string XA, a second encoding section for encoding a data string XA' after inter leaving and two puncturing sections

for puncturing the outputs of the first and second encoding sections.

[0008] However, the conventional CDMA transmission apparatus has a problem that when a plurality of spread signals is assigned to one user signal for spreading to improve its error rate characteristic, the number of user signals that can be multiplexed is reduced, and therefore the frequency utilization efficiency is reduced and when the spreading factor is increased, the error rate characteristic may be improved but the transmission efficiency decreases. That is, the conventional CDMA transmission apparatus has a problem that it is difficult to make improvement of the error rate characteristic compatible with improvement of the transmission efficiency.

Disclosure of Invention

[0009] It is an object of the present invention to provide a transmission apparatus and transmission method capable of making both the error rate characteristic and the transmission efficiency mutually more compatible.

[0010] This object is to adaptively modulate the systematic bit data and parity bit data independently of each other. That is, this object can be realized, when channel quality deteriorates, by converting the systematic bit data to a modulation scheme with a small number of multi-values to prevent a reduction of the error rate characteristic, and when an error occurs, by carrying out error correction using parity bits transmitted according to a modulation scheme having more multi-values than the systematic bit data and reducing the number of retransmissions of transmission data.

[0011] Furthermore, this object can be attained through a CDMA transmission apparatus which uses turbo codes as error correcting codes by carrying out spreading with a plurality of spreading codes assigned to only systematic bits.

Brief Description of Drawings

[0012]

FIG.1 is a block diagram showing a configuration of a conventional transmission apparatus;

FIG.2 is a block diagram showing a configuration of a transmission apparatus according to Embodiment 1 of the present invention;

FIG.3 is a block diagram showing a configuration of an encoding section according to Embodiment 1 of the present invention;

FIG.4 is a flow chart showing an operation of a transmission apparatus according to Embodiment 1 of the present invention;

FIG.5 is a block diagram showing a configuration of a transmission apparatus according to Embodiment 2 of the present invention;

FIG.6 is a block diagram showing a configuration of

a control section according to Embodiment 2 of the present invention;

FIG.7 is a flow chart showing operation of a transmission apparatus according to Embodiment 2 of the present invention;

FIG.8 is a block diagram showing a configuration of a transmission apparatus according to Embodiment 3 of the present invention;

FIG.9 is a block diagram showing a configuration of a transmission apparatus according to Embodiment 4 of the present invention;

FIG.10 is a block diagram showing a configuration of a transmission apparatus according to Embodiment 5 of the present invention;

FIG.11 is a block diagram showing a configuration of a transmission apparatus according to Embodiment 6 of the present invention; and

FIG.12 illustrates operation of a transmission apparatus according to Embodiment 6 of the present invention.

Best Mode for Carrying out the Invention

[0013] With reference now to the attached drawings, embodiments of the present invention will be explained in detail below.

(Embodiment 1)

[0014] FIG.2 illustrates a configuration of a transmission apparatus 100 according to Embodiment 1 of the present invention. Here, Embodiment 1 will describe a case where two kinds of modulation schemes, QPSK and 16QAM are used. Furthermore, the modulation scheme with more multi-values according to Embodiment 1 is 16QAM and the modulation scheme with fewer multi-values is QPSK.

[0015] The transmission apparatus 100 is mainly constructed of a control section 101, a turbo encoding section 102, a modulation section 103, a P/S conversion section 105, a spreader 106, a serial/parallel (hereinafter referred to as "S/P") conversion section 107, an inverse discrete Fourier transform section (IDFT) 108 and an antenna 109. Furthermore, the modulation section 103 is mainly constructed of a first modulation section 103a and a second modulation section 103b.

[0016] The control section 101 decides communication quality using an RSSI (Received Signal Strength Indicator) signal, and outputs a control signal according to communication quality to the second modulation section 103b. That is, when the RSSI signal is equal to or higher than a threshold, a control signal for setting the modulation scheme to 16QAM which is a modulation scheme with more multi-values is output to the second modulation section 103b. On the other hand, when the RSSI signal is less than the threshold, a control signal for setting the modulation scheme to QPSK which is a modulation scheme with fewer multi-values is output to

the second modulation section 103b. Now, when a communication is currently in progress, and as a result of a decision in the control section 101, if the currently used modulation scheme is continuously used, the control section 101 does not output any control signal to the second modulation section 103b. Here, the modulation scheme with more multi-values refers to a modulation scheme with many constellation mapping points on an I-Q plane.

[0017] The turbo encoding section 102 is, for example, a turbo encoder, and outputs part of the input transmission data to the first modulation section 103a as systematic bit data without encoding the transmission data, and carries out recursive convolutional encoding on part of the remaining transmission data input and outputs it as parity bit data to the second modulation section 103b. Here, the turbo encoding section 102 may also be any encoder other than the turbo encoder. Details of the turbo encoding section 102 will be explained in detail later.

[0018] The modulation scheme of the first modulation section 103a is fixed to QPSK, carries out QPSK modulation on the systematic bit data input from the turbo encoding section 102 and outputs the modulated data to the P/S conversion section 105.

[0019] The second modulation section 103b carries out adaptive modulation of QPSK modulation or 16QAM modulation based on the control signal input from the control section 101 on the parity bit data input from the turbo encoding section 102 and outputs the modulated data to the P/S conversion section 105. The method of changing the modulation scheme will be explained later.

[0020] The P/S conversion section 105 converts the systematic bit data input from the first modulation section 103a and the parity bit data input from the second modulation section 103b from parallel data to serial data and outputs the serial data to the spreader 106.

[0021] The spreader 106 which is the spreading section multiplies the transmission data input from the S/P conversion section 107 by a spreading code and outputs the multiplication result to the S/P conversion section 107. Here, when transmission is carried out according to the OFDM-CDMA communication scheme, a spreading code whose spreading factor is other than "1" is multiplied and when transmission is carried out according to the OFDM communication scheme, a spreading code whose spreading factor is "1" is multiplied.

[0022] The S/P conversion section 107 converts the transmission data input from the spreader 106 from serial data to parallel data and outputs the conversion result to the inverse discrete Fourier transform section 108.

[0023] The inverse discrete Fourier transform section 108 transforms data into a sum of N (N is an arbitrary natural number) subcarriers with different frequencies to generate transmission data, and sends the transmission data generated from the antenna 109. When transmission is carried out according to the OFDM-CDMA communication scheme, the signal multiplexing number

of each subcarrier is a multiplexing number other than "1" and the code multiplexing number of the transmission signal is other than "1" and when transmission is carried out according to the OFDM communication scheme, the signal multiplexing number of each subcarrier is "1" and the code multiplexing number of the transmission signal is "1". Furthermore, the S/P conversion section 107 and inverse discrete Fourier transform section 108 constitute an orthogonal frequency division multiplexing section.

[0024] Then, the configuration of the turbo encoding section 102 will be explained in more detail using FIG. 3. The turbo encoding section 102 is mainly constructed of an interleaver 201, a convolutional encoding section 202 and a convolutional encoding section 203.

[0025] The interleaver 201 interleaves the transmission data and outputs the interleaved data to the convolutional encoding section 203.

[0026] The convolutional encoding section 202 carries out recursive convolutional encoding on part of the transmission data and outputs the data to the second modulation section 103b. The output from the convolutional encoding section 202 is the parity bit data.

[0027] The convolutional encoding section 203 carries out recursive convolutional encoding on part of the transmission data input from the interleaver 201 and outputs the data to the second modulation section 103b. The output from the convolutional encoding section 203 is the parity bit data. Here, part of the transmission data input to the turbo encoding section 102 is not encoded and output as is. This output is the systematic bit data.

[0028] Then, the operation of the transmission apparatus 100 will be explained using FIG. 4. The transmission data is encoded by the turbo encoding section 102 (step (hereinafter referred to as "ST") 301), the systematic bit data is output to the first modulation section 103a, and the parity bit data is output to the second modulation section 103b. The systematic bit data input to the first modulation section 103a is QPSK-modulated and output to the P/S conversion section 105 (ST302).

[0029] The control section 101 decides whether the RSSI signal is equal to or higher than a threshold based on the RSSI signal (ST303), and when the RSSI signal is equal to or higher than the threshold, the control section 101 decides that the communication quality is good and outputs a control signal for setting the modulation scheme to 16QAM to the second modulation section 103b. The second modulation section 103b sets the modulation scheme to 16QAM based on a control signal for setting the modulation scheme input from the control section 101 (ST304). The parity bit data input to the second modulation section 103b is 16QAM-modulated and output to the P/S conversion section 105 (ST305).

[0030] On the other hand, when the RSSI signal is less than the threshold in ST303, the control section 101 outputs a control signal for setting the modulation scheme to QPSK to the second modulation section 103b. The second modulation section 103b sets the

modulation scheme to QPSK based on the control signal for setting the modulation scheme input from the control section 101 (ST306). The parity bit data input to the second modulation section 103b is QPSK-modulated and output to the P/S conversion section 105 (ST307). The P/S conversion section 105 converts the systematic bit data and parity bit data from parallel data to serial data and output the serial data to the spreader 106 (ST308). Then, the transmission data is multiplied by a spreading code by the spreader 106 (ST309), further subjected to S/P conversion processing which is orthogonal frequency division multiplexing processing and inverse discrete Fourier transform processing and output from the antenna 109 (ST310).

[0031] When the channel quality deteriorates in such a transmission apparatus, deterioration of the error rate characteristic of the systematic bit data is prevented by setting a modulation scheme with fewer multi-values, and if an error still occurs, error correction can be performed according to a modulation scheme with more multi-values than the systematic bit data and the number of retransmissions of transmission data is reduced.

[0032] On the other hand, when the channel quality is good, the error rate characteristic can be improved by setting the systematic bit data to a modulation scheme with more multi-values and if an error still occurs, error correction can be performed using parity bits transmitted according to a modulation scheme at such a level that there are fewer multi-values than the systematic bit data but the error rate characteristic does not deteriorate, and the number of retransmissions of the transmission data is reduced.

[0033] Thus, according to Embodiment 1, systematic bit data and parity bit data are modulated independently of each other and the modulation scheme of the parity bit data is adaptively changed according to the communication quality, and it is therefore possible to make improvement of the error rate characteristic compatible with improvement of the transmission efficiency. Furthermore, independently modulated systematic bit data and parity bit data are subjected to spreading processing and then subjected to orthogonal frequency division multiplexing, and therefore it is possible to further improve the error rate characteristic of the transmission data.

[0034] According to Embodiment 1, only parity bit data is adaptively modulated according to communication quality, but the embodiment can also be adapted so that only systematic bit data is adaptively modulated according to communication quality.

(Embodiment 2)

[0035] FIG.5 illustrates a configuration of a transmission apparatus 400 according to Embodiment 2 of the present invention. Here, this Embodiment 2 will describe a case where two types of modulation schemes; QPSK

and 16QAM are used. Furthermore, in this Embodiment 2, the modulation scheme with more multi-values is 16QAM and the modulation scheme with fewer multi-values is QPSK.

[0036] A modulation section 402 in this Embodiment 2 is mainly constructed of a first modulation section 402a and a second modulation section 402b. This Embodiment 2 is characterized in that the modulation schemes in both the first modulation section 402a and second modulation section 402b are adaptively modulated according to communication quality. Note that this embodiment is different from FIG. 2 in that a control signal from the control section 401 is input to the first modulation section 402a, and explanations of the same components as those in Embodiment 1 will be omitted.

[0037] The control section 401 decides communication quality using an RSSI signal and outputs a control signal according to the communication quality to the first modulation section 402a and second modulation section 402b. When setting a modulation scheme, the control section 401 uses two types of thresholds, threshold α for setting a modulation scheme when modulating systematic bit data and threshold β for setting a modulation scheme for modulating parity bit data. That is, when the RSSI signal is equal to or higher than the threshold α , a control signal for setting the modulation scheme to 16QAM which is a modulation scheme with more multi-values is output to the first modulation section 402a and when the RSSI signal is equal to or higher than the threshold β , a control signal for setting the modulation scheme to 16QAM which is a modulation scheme with more multi-values is output to the second modulation section 402b.

[0038] On the other hand, when the RSSI signal is less than the threshold α , the control section 401 outputs a control signal for setting the modulation scheme to QPSK which is a modulation scheme with fewer multi-values to the first modulation section 402a and when the RSSI signal is less than the threshold β , the control section 401 outputs a control signal for setting the modulation scheme to QPSK which is a modulation scheme with fewer multi-values to the second modulation section 402b. Here, when a communication is currently in progress and a decision result by the control section 401 shows that the currently used modulation scheme continues to be used, the control section 401 need not output any control signal to the outputs of the first modulation section 402a and second modulation section 402b. The configuration of the control section 401 will be explained in more detail later.

[0039] Based on the control signal input from the control section 401, the first modulation section 402a carries out QPSK modulation or 16QAM modulation on the systematic bit data input from the turbo encoding section 102 and outputs the modulated data to the P/S conversion section 105.

[0040] Based on the control signal input from the control section 401, the second modulation section 402b

carries out adaptive modulation such as QPSK modulation or 16QAM modulation on the parity bit data input from the turbo encoding section 102, and outputs the modulated data to the P/S conversion section 105. The method of changing the modulation scheme will be described later.

[0041] Then, the configuration of the control section 401 will be explained in more detail using FIG. 6. The control section 401 is mainly constructed of a first decision control section 501 and a second decision control section 502.

[0042] When the RSSI signal is equal to or higher than a preset threshold α , the first decision control section 501 outputs a control signal for setting the modulation scheme to 16QAM to the first modulation section 402. On the other hand, when the RSSI signal is less than the threshold α , the first decision control section 501 outputs a control signal for setting the modulation scheme to QPSK to the first modulation section 402a.

[0043] When the RSSI signal is equal to or higher than a preset threshold β , the second decision control section 502 outputs a control signal for setting the modulation scheme to 16QAM to the first modulation section 402. On the other hand, when the RSSI signal is less than the threshold β , the second decision control section 502 outputs a control signal for setting the modulation scheme to QPSK to the second modulation section 402b.

[0044] Since systematic bit data is required to have better communication quality than parity bit data, the threshold α is set to a higher RSSI value than the threshold β . In this way, systematic bit data is always modulated according to the same modulation scheme as that of parity bit data or according to the modulation scheme with fewer multi-values than the modulation scheme of parity bit data.

[0045] Then, the operation of the transmission apparatus 400 will be explained using FIG. 7. The transmission data is encoded by the turbo encoding section 102 (ST601), systematic bit data is output to the first modulation section 402a and parity bit data is output to the second modulation section 402b.

[0046] Based on the RSSI signal, the control section 401 decides whether the RSSI signal is equal to or higher than threshold α (ST602), and when the RSSI signal is equal to or higher than the threshold α , the control section 401 considers that the communication quality is good and outputs a control signal for setting the modulation scheme to 16QAM to the first modulation section 402a. Based on a control signal for setting the modulation scheme input from the control section 401, the first modulation section 402a sets the modulation scheme to 16QAM (ST603). The systematic bit data input to the first modulation section 402a is 16QAM-modulated and output to the P/S conversion section 105 (ST604).

[0047] On the other hand, in ST602, when the RSSI signal is less than the threshold α , the control section 401 outputs a control signal for setting the modulation

scheme to QPSK. Based on a control signal for setting the modulation scheme input from the control section 401, the first modulation section 402a sets the modulation scheme to QPSK (ST605). The systematic bit data input to the second modulation section 402b is QPSK-modulated and output to the P/S conversion section 105 (ST606).

[0048] Then, based on the RSSI signal, the control section 401 decides whether the RSSI signal is equal to or higher than the threshold β or not (ST607), and when the RSSI signal is equal to or higher than the threshold β , the control section 401 considers that the communication quality is good and outputs a control signal for setting the modulation scheme to 16QAM to the second modulation section 402b. Based on a control signal for setting the modulation scheme input from the control section 401, the second modulation section 402b sets the modulation scheme to 16QAM (ST608). The parity bit data input to the second modulation section 402b is 16QAM-modulated and output to the P/S conversion section 105 (ST609).

[0049] On the other hand, in ST607, when the RSSI signal is less than the threshold β , the control section 401 outputs a control signal for setting the modulation scheme to QPSK to the second modulation section 402b. Based on a control signal for setting the modulation scheme input from the control section 401, the second modulation section 402b sets the modulation scheme to QPSK (ST610).

[0050] The parity bit data input to the second modulation section 402b is QPSK-modulated and output to the P/S conversion section 105 (ST611). The P/S conversion section 105 converts the systematic bit data and parity bit data from parallel to serial and outputs the serial data to the spreader 106 (ST612). Then, the transmission data is multiplied by a spreading code by the spreader 106 (ST613), and further subjected to S/P conversion processing which is orthogonal frequency division multiplexing processing, and subjected to inverse discrete Fourier transform processing and sent from the antenna 109 (ST614).

[0051] Thus, according to Embodiment 2, systematic bit data is adaptively modulated according to communication quality in addition to the effects in Embodiment 1, and it is therefore possible to further make improvement of the error rate characteristic compatible with improvement of transmission efficiency. Furthermore, according to Embodiment 2, the control section 401 compares the RSSI signal with different thresholds for systematic bit data and parity bit data, and therefore it is possible to make improvement of the error rate characteristic compatible with improvement of transmission efficiency by flexibly responding to changes in communication quality.

[0052] This Embodiment 2 uses different thresholds α and β to be used when deciding whether communication quality is equal to or higher than that of an RSSI signal, but it is also possible to set the same value for

the threshold α and threshold β or set the threshold α to a value smaller than the threshold β .

(Embodiment 3)

[0053] FIG. 8 illustrates a configuration of a transmission apparatus 700 according to Embodiment 3 of the present invention. Here, when two types of modulation schemes QPSK and 16QAM are used in Embodiment 3, a case where QPSK is used as a modulation scheme with fewer multi-values will be explained. In this Embodiment 3, the modulation scheme with more multi-values is 16QAM and the modulation scheme with fewer multi-values is QPSK.

[0054] This Embodiment 3 is characterized in that while transmission data (other data) for which good reception quality is not required is modulated according to 16QAM with more multi-values, when transmitting transmission data for which good reception quality is required, the modulation schemes for both the first modulation section 103a and second modulation section 103b are always set to QPSK which is a modulation scheme with fewer multi-values. Here, the same components as those in FIG. 2 are assigned the same reference numerals and explanations thereof will be omitted.

[0055] The first modulation section 103a modulates the transmission data according to QPSK and outputs the modulated data to the P/S conversion section 105.

[0056] The second modulation section 103b modulates the transmission data according to QPSK and outputs the modulated data to the P/S conversion section 105.

[0057] The transmission data according to this embodiment requires good communication quality such as control information and retransmission information. The transmission data is not limited to control information and retransmission information, but includes that for which good communication quality is required.

[0058] Thus, according to this Embodiment 3, the systematic bit data and parity bit data are modulated independently of each other and the modulation scheme is fixed to a modulation scheme with fewer multi-values, and therefore when the transmission data is required to have good communication quality such as control information and retransmission information, it is possible to make improvement of the error rate characteristic compatible with improvement of the transmission efficiency. Furthermore, the independently modulated systematic bit data and parity bit data are subjected to spreading processing and then subjected to orthogonal frequency division multiplexing, and therefore it is possible to further improve an error rate characteristic of transmission data.

[0059] In this Embodiment 3, the modulation scheme is set to QPSK, but the modulation scheme is not limited to QPSK and any modulation scheme other than QPSK can be used if it has at least fewer multi-values. Furthermore, according to this Embodiment 3, both the first

modulation section 103a and second modulation section 103b are modulated according to QPSK, but they need not always be set to the same modulation scheme and the first modulation section 103a and second modulation section 103b can be set to different modulation schemes.

[0060] Note that since systematic bit data is required to have better communication quality than parity bit data, if 16QAM is used as the modulation scheme for both systematic bit data and parity bit data, a method of carrying out processing that the systematic bit data is arranged on polarity bits is also available. This allows the systematic bit data to obtain a good error rate characteristic.

[0061] However, the quality of the 16QAM polarity bits is inferior to the quality of QPSK, and therefore even if the systematic bit is arranged on polarity bits, the error rate characteristic deteriorates compared to Embodiment 1 and Embodiment 2. Furthermore, the processing of arranging systematic bits on polarity bits requires special processing of dividing the transmission data into two systems and arranging those data on the 16QAM polarity bits and amplification bits, and therefore the processing is more complicated than that in Embodiment 1 and Embodiment 2. Furthermore, the processing of arranging systematic bits on polarity bits cannot be performed with 8PSK, QPSK and BPSK, but there is no restriction on the modulation schemes in the cases of Embodiment 1 and Embodiment 2.

(Embodiment 4)

[0062] FIG.9 is a block diagram showing a configuration of a transmission apparatus according to Embodiment 4 of the present invention.

[0063] As shown in FIG.9, the transmission apparatus 800 according to Embodiment 4 of the present invention is provided with a turbo encoding section 801, a spreading apparatus 802, a spreading section 803, a parallel/serial (P/S) conversion section 804, a radio transmission section 805 and an antenna 806. The spreading apparatus 802 includes a plurality of spreading sections 802-1, 802-2 and an addition section 802-3.

[0064] The input terminals of the spreading sections 802-1, 802-2 and spreading section 803 are connected to the output terminals of the turbo encoding section 801. The input terminals of the addition section 802-3 are connected to the output terminals of the spreading sections 802-1, 802-2. The input terminals of the parallel/serial conversion section 804 are connected to the output terminals of the addition section 802-3 and spreading section 803. The input terminal of the radio transmission section 805 is connected to the output terminal of the parallel/serial conversion section 804. The input terminal of the antenna 806 is connected to the output terminal of the radio transmission section 805.

[0065] The turbo encoding section 801 receives a transmission signal, converts the signal to a turbo code,

generates systematic bit data and parity bit data, gives the systematic bit data to the spreading sections 802-1, 802-2 and gives the parity bit data to the spreading section 803.

[0066] The spreading apparatus 802 assigns a plurality of different spreading codes to the systematic bit data from the turbo encoding section 801, spreads the systematic bit data, multiplexes the spread systematic bit data and gives the multiplexed systematic bit data to the parallel/serial conversion section 804.

[0067] That is, the spreading sections 802-1, 802-2 assign different spreading codes to the systematic bit data from the turbo encoding section 801, spread the systematic bit data and give the spread systematic bit data to the addition section 802-3. The addition section 802-3 receives the spread systematic bit data from the spreading sections 802-1, 802-2, multiplexes the data and gives the multiplexed systematic bit data to the parallel/serial conversion section 804.

[0068] Furthermore, the spreading section 803 receives the parity bit data from the turbo encoding section 801, spreads the parity bit data with one spreading code assigned thereto and gives the spread parity bit data to the P/S conversion section 804.

[0069] The P/S conversion section 804 modulates the systematic bit data from the addition section 802-3 and the parity bit data from the spreading section 803 into one serial modulated signal and sends the modulated signal to the radio transmission section 805. The radio transmission section 805 receives the modulated signal from the P/S conversion section 804 and sends the modulated signal through the antenna 806.

[0070] Embodiment 4 of the present invention may also be adapted so that the spreading apparatus 802 spreads the systematic bit data with three or more different spreading codes assigned to the systematic bit data, multiplexes the spread systematic bit data and gives the spread systematic bit data to the P/S conversion section 804. Furthermore, in Embodiment 1 of the present invention, the number of spreading codes of the spreading apparatus 802 may be made variable based on the channel quality (e.g., RSSI) and multipath delay distribution time information. Furthermore, in Embodiment 4 of the present invention, it is also possible to assign a plurality of spreading codes to parity bit data or make a plurality of spreading codes of parity bit data variable or make the plural number of spreading codes of parity bit data variable based on channel quality (e.g., RSSI) and multipath delay distribution time information.

[0071] Thus, according to Embodiment 4 of the present invention, since more spreading codes are assigned to systematic bit data which is required to have good channel quality than spreading codes assigned to parity bit data whose channel quality can be low, it is possible to make improvement of the error rate characteristic compatible with improvement of the transmission efficiency.

(Embodiment 5)

[0072] Embodiment 5 of the present invention will be explained in detail with reference to the attached drawings. FIG.10 is a block diagram showing a configuration of a transmission apparatus according to Embodiment 5 of the present invention. In Embodiment 5 of the present invention, the same components as those in Embodiment 4 of the present invention are assigned the same reference numerals and explanations thereof will be omitted.

[0073] As shown in FIG.10, a transmission apparatus 900 according to Embodiment 5 of the present invention is provided with a turbo encoding section 801, a spreading section 901, a spreading section 803, a spreading factor setting section 902, a parallel/serial conversion section 804, a radio transmission section 805 and an antenna 806.

[0074] The input terminal of spreading section 901 is connected to the output terminal of the turbo encoding section 801. The output terminal of the spreading section 901 is connected to the input terminal of the P/S conversion section 804. The output terminals of the spreading factor setting section 902 are connected to the control terminals of the spreading section 803 and spreading section 901. The spreading factor setting section 902 sets a first spreading factor and a second spreading factor which is greater than the first spreading factor, gives the first spreading factor to the spreading section 803 and gives the second spreading factor to the spreading section 901.

[0075] The turbo encoding section 801 receives a transmission signal, transforms it to a turbo code, generates a systematic bit data and parity bit data, gives the systematic bit data to the spreading section 901 and gives the parity bit data to the spreading section 803.

[0076] The spreading section 803 receives the parity bit data from the turbo encoding section 801, spreads the parity bit data with one spreading code assigned thereto and gives the spread parity bit data to the P/S conversion section 804.

[0077] Furthermore, the spreading section 901 assigns one spreading code to the systematic bit data from the turbo encoding section 801, spreads the systematic bit data using the second spreading factor and gives the spread systematic bit data to the P/S conversion section 804.

[0078] Embodiment 5 of the present invention can also be adapted so that the spreading apparatus 802 shown in FIG. 9 is provided instead of the spreading section 901, a plurality of spreading factors greater than the first spreading factor is given to the spreading sections 802-1, 802-2 of the spreading apparatus 802 and the systematic bit data is spread using the plurality of spreading factors with a plurality of spreading codes assigned thereto.

[0079] Furthermore, in Embodiment 5 of the present invention, the spreading factors of the spreading sec-

tions 803, 901 can also be made variable based on the channel quality (e.g., RSSI) and multipath delay distribution time information. Furthermore, either one of the spreading factors of the spreading sections 803, 901 may also be made adaptively variable. Furthermore, in Embodiment 5 of the present invention, the parity bit data may also be spread with a plurality of spreading factors, and the plurality of spreading factors of parity bit data may also be made variable and the plurality of spreading factors of parity bit data may also be made variable based on channel quality (e.g., RSSI) and multipath delay distribution time information.

[0080] Thus, according to Embodiment 5 of the present invention, the spreading factor of systematic bit data which is required to have good channel quality is set to a value greater than the spreading factor of parity bit data, and therefore it is possible to make improvement of the error rate characteristic compatible with improvement of the transmission efficiency.

(Embodiment 6)

[0081] Then, Embodiment 6 of the present invention will be explained in detail with reference to the attached drawings. FIG.11 is a block diagram showing a configuration of a transmission apparatus according to Embodiment 6 of the present invention. FIG.11 illustrates the operation of the transmission apparatus according to Embodiment 6 of the present invention. In Embodiment 6 of the present invention, the same components as those in Embodiment 4 of the present invention are assigned the same reference numerals and explanations thereof will be omitted.

[0082] As shown in FIG.11, the transmission apparatus 1000 according to Embodiment 6 of the present invention is constructed of the transmission apparatus 800 according to Embodiment 4 of the present invention with an orthogonal frequency division multiplexing apparatus 1001 added.

[0083] That is, the transmission apparatus 1000 according to Embodiment 6 of the present invention is provided with a turbo encoding section 801, a spreading apparatus 802, a spreading section 803, a parallel/serial conversion section 804, an orthogonal frequency division multiplexing (OFDM) apparatus 1001, a radio transmission section 805 and an antenna 806. The spreading apparatus 802 is provided with a plurality of spreading sections 802-1, 802-2 and an addition section 802-3.

[0084] The input terminal of the orthogonal frequency division multiplexing apparatus 1001 is connected to the output terminal of the P/S conversion section 804. The output terminal of the orthogonal frequency division multiplexing apparatus 1001 is connected to the input terminal of the radio transmission section 805.

[0085] The orthogonal frequency division multiplexing apparatus 1001 receives a modulated signal from the P/S conversion section 804, applies orthogonal fre-

quency division multiplexing thereto, generates an OFDM-CDMA signal and gives the signal to the radio transmission section 805. The radio transmission section 805 receives the OFDM-CDMA signal from the orthogonal frequency division multiplexing apparatus 1001 and sends the signal through the antenna 806.

[0086] The orthogonal frequency division multiplexing apparatus 1001 sets a spreading ratio to, for example, as shown in FIG. 12, 1/5 of the number of subcarriers, divides all subcarriers into five groups G1 to G5 and arranges systematic bit data and parity bit data of modulated signals from the parallel/serial conversion section 104 on subcarriers of the respective groups G1 to G5. In this case, the parallel/serial conversion section 804 rearranges the modulated signals into a subcarrier configuration as shown in FIG. 12. Note that the spreading ratio is not limited to 1/5 of the number of subcarriers, but can be any value. Furthermore, the spreading ratio of each group of subcarriers need not always be the same, but can be set to any value.

[0087] Thus, in addition to the effects of Embodiment 4, Embodiment 6 of the present invention provides a communication scheme combining a CDMA communication scheme and multicarrier communication scheme (including OFDM scheme) (generally called "multicarrier CDMA scheme") whereby the signal multiplexing number is individually set for each subcarrier for which the number of spreading codes assigned to user signals to be retransmitted is increased as the number of retransmissions increases, and can thereby prevent the number of retransmissions from excessively increasing in a multipath environment.

[0088] Furthermore, according to Embodiment 6 of the present invention, the multicarrier CDMA scheme can remove interference between neighboring codes in a multipath environment by guard sections, and can thereby drastically improve the error rate in a multipath environment compared to the CDMA scheme and further prevent the number of retransmissions from increasing.

[0089] According to Embodiment 1 and Embodiment 2, adaptive modulation is carried out using 16QAM and QPSK, but adaptive modulation is not limited to 16QAM and QPSK and adaptive modulation can also be carried out using modulation schemes other than 16QAM and QPSK such as 8PSK and BPSK. Furthermore, in Embodiment 1 and Embodiment 2, the systematic bit data is modulated by the first modulation section and parity bit data is modulated by the second modulation section, but the present invention is not limited to this and can be adapted so that one modulation section modulates the systematic bit and parity bit. Furthermore, in Embodiment 1 and Embodiment 2, communication quality is decided based on an RSSI signal, but the present invention is not limited to this and if there are signals, etc., other than the RSSI signal whereby communication quality can be decided, the present invention can also be adapted so that communication quality is decided by

signals, etc., other than the RSSI signal. Furthermore, the transmission apparatus and transmission method according to Embodiment 1 to Embodiment 6 are applicable to any communication schemes such as CDMA, OFDM, OFDM-CDMA, multicarrier CDMA and single carrier. Furthermore, the transmission apparatus and transmission method according to Embodiment 1 to Embodiment 6 are applicable to a base station apparatus and communication terminal apparatus.

[0090] As described above, the present invention can further make the error rate characteristic compatible with the transmission efficiency.

[0091] This application is based on the Japanese Patent Application No. 2002-317728 filed on October 31, 2002 and the Japanese Patent Application No. 2002-350026 filed on December 2, 2002, entire content of which is expressly incorporated by reference herein.

Industrial Applicability

[0092] The present invention relates to a transmission apparatus and a transmission method and is preferably applicable to a transmission apparatus and a transmission method which transmits transmission data encoded using a turbo encoder in particular.

Claims

1. A transmission apparatus comprising:

an encoding section that turbo-encodes transmission data and outputs systematic bit data and parity bit data;

a modulation section that modulates said systematic bit data and said parity bit data independently of each other; and

a transmission section that transmits systematic bit data and parity bit data modulated by said modulation section.

2. The transmission apparatus according to claim 1, wherein said modulation section fixes the modulation scheme of one of systematic bit data and parity bit data and adaptively changes the modulation scheme of the other one of systematic bit data and parity bit data.

3. The transmission apparatus according to claim 1, wherein said modulation section adaptively changes both modulation schemes of systematic bit data and parity bit data.

4. The transmission apparatus according to claim 3, wherein said modulation section has better communication quality when the modulation scheme of systematic bit data is changed than communication quality when the modulation scheme of parity bit data

ta is changed.

5. The transmission apparatus according to claim 1, wherein when the communication quality of said transmission data is required to be better than the communication quality of other data, said modulation section modulates the systematic bit data and parity bit data according to a modulation scheme with fewer multi-values than the modulation scheme used for said other data. 5 10
6. The transmission apparatus according to claim 5, wherein said transmission data is information used for communication control or retransmission information. 15
7. The transmission apparatus according to claim 1, wherein said transmission section comprises a spreading section that spreads said modulated systematic bit data and parity bit data and an orthogonal frequency division multiplexing section that applies orthogonal frequency division multiplexing to the spread signal. 20
8. The transmission apparatus according to claim 6, wherein the spreading factor of said spreading section is set to "1" and the code multiplexing number of the transmission signal is set to "1". 25
9. A base station apparatus provided with a transmission apparatus, said transmission apparatus comprising: 30
 - an encoding section that turbo-encodes transmission data and outputs systematic bit data and parity bit data; 35
 - a modulation section that modulates said systematic bit data and said parity bit data independently of each other; and
 - a transmission section that transmits said systematic bit data and said parity bit data modulated by said modulation section. 40
10. A communication terminal apparatus provided with a transmission apparatus, said transmission apparatus comprising: 45
 - an encoding section that turbo-encodes transmission data and outputs systematic bit data and parity bit data; 50
 - a modulation section that modulates said systematic bit data and said parity bit data independently of each other; and
 - a transmission section that transmits said systematic bit data and said parity bit data modulated by said modulation section. 55
11. A transmission method comprising:

a step of turbo-encoding transmission data and outputting systematic bit data and parity bit data;

a step of modulating systematic bit data according to a fixed modulation scheme;

a step of adaptively modulating the parity bit data independently of the systematic bit data; and
a step of transmitting the modulated systematic bit data and the modulated parity bit data.

10 CDMA TRANSMISSION APPARATUS

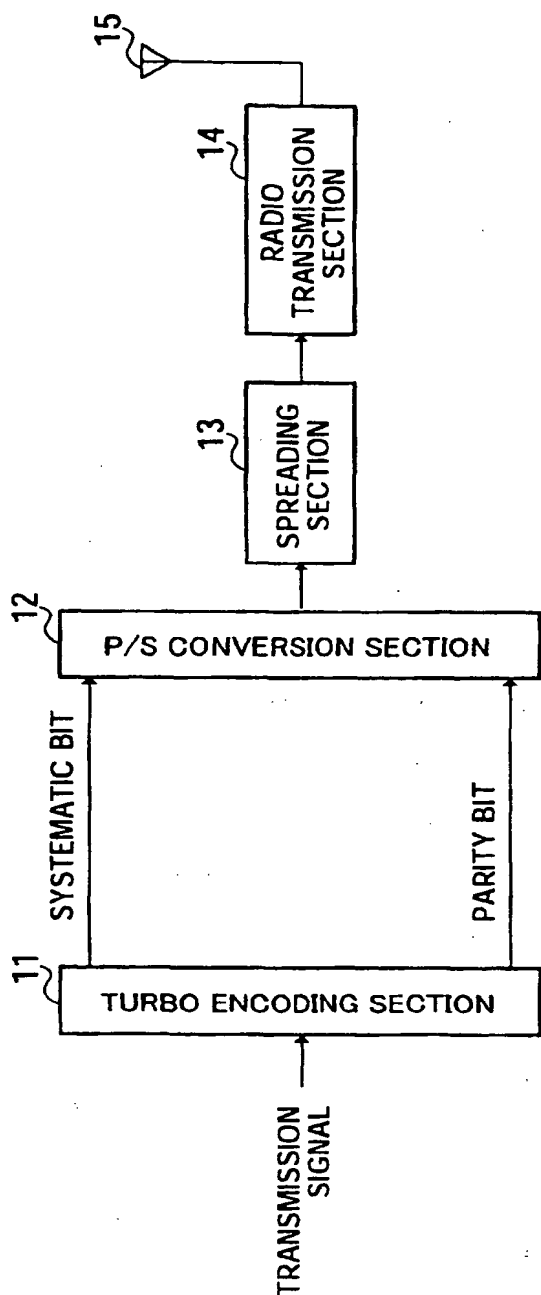


FIG. 1

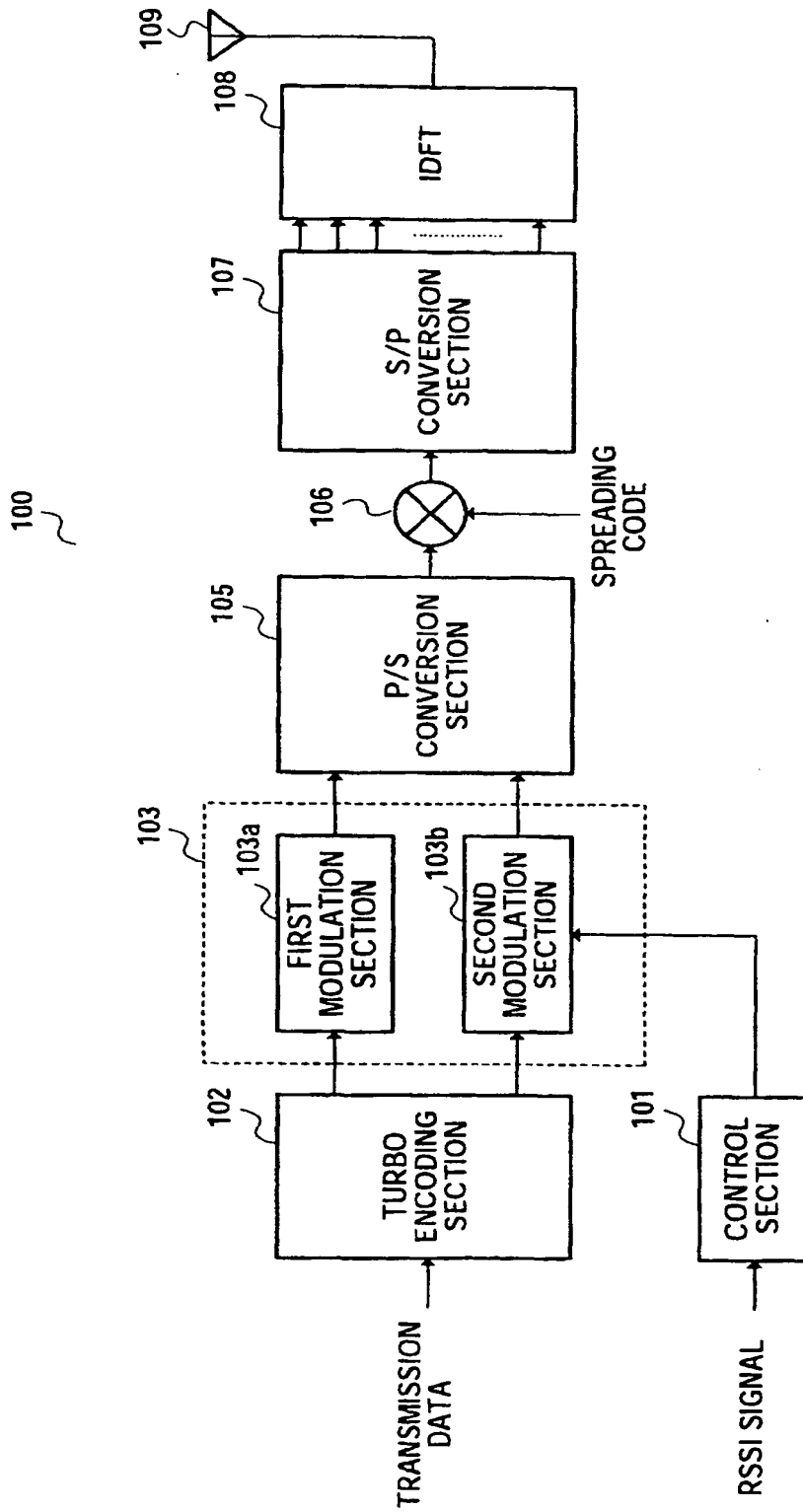


FIG.2

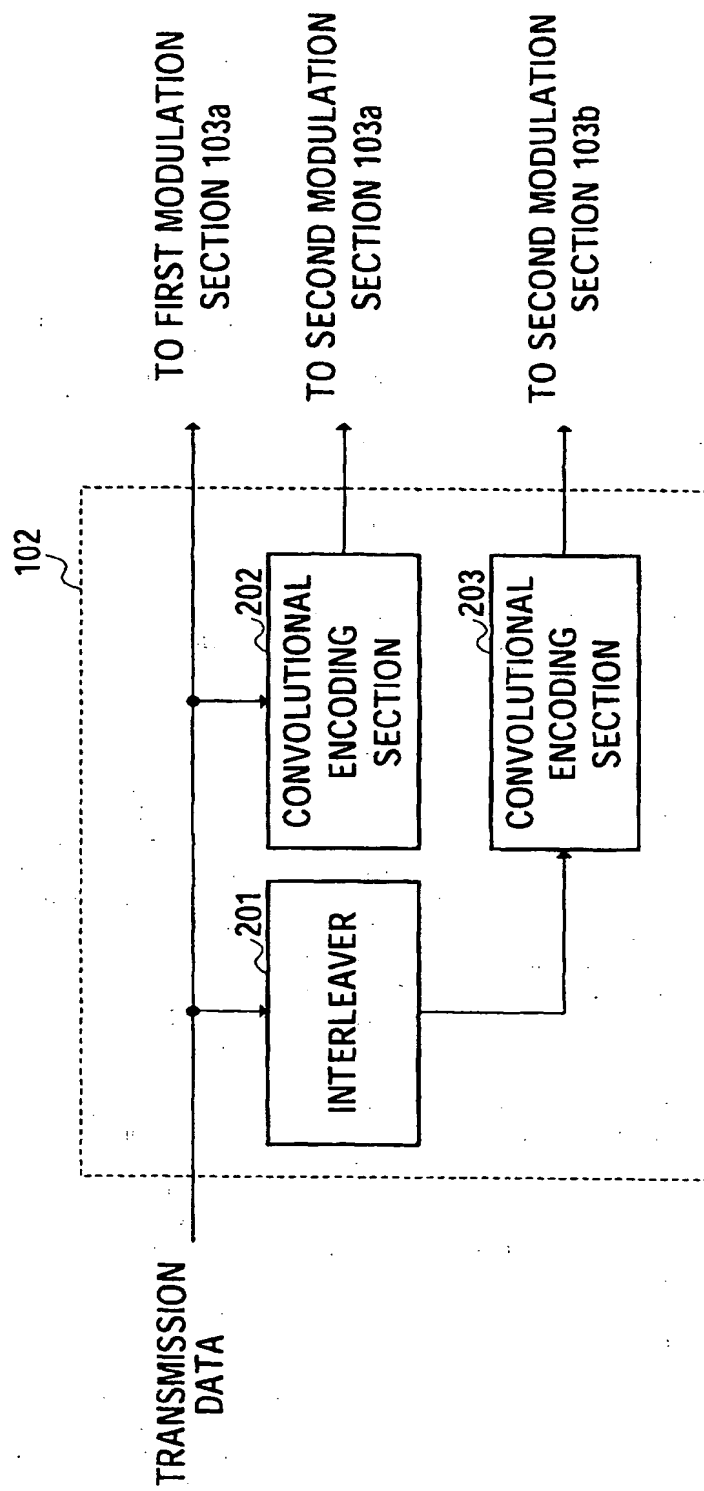


FIG.3

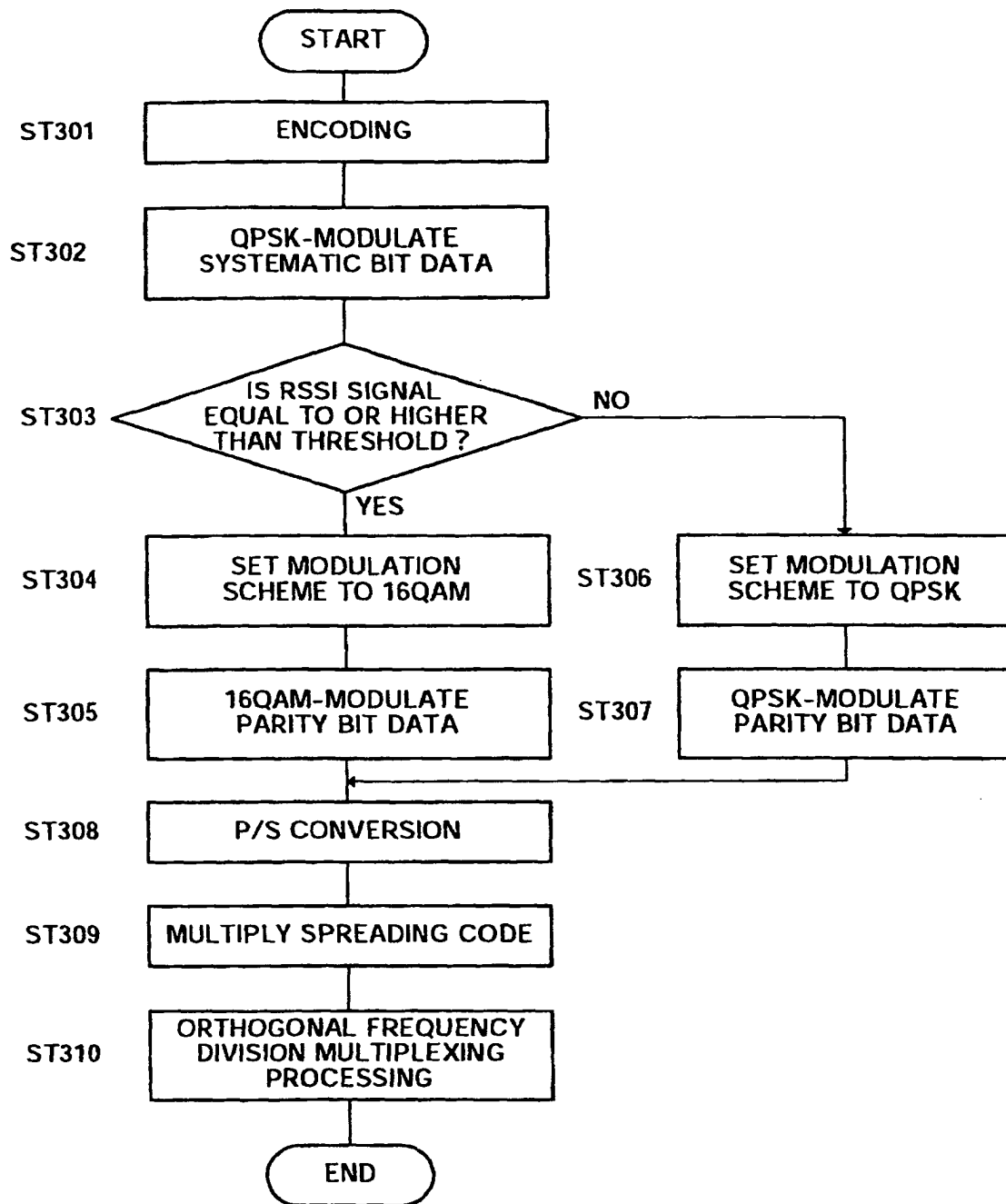


FIG.4

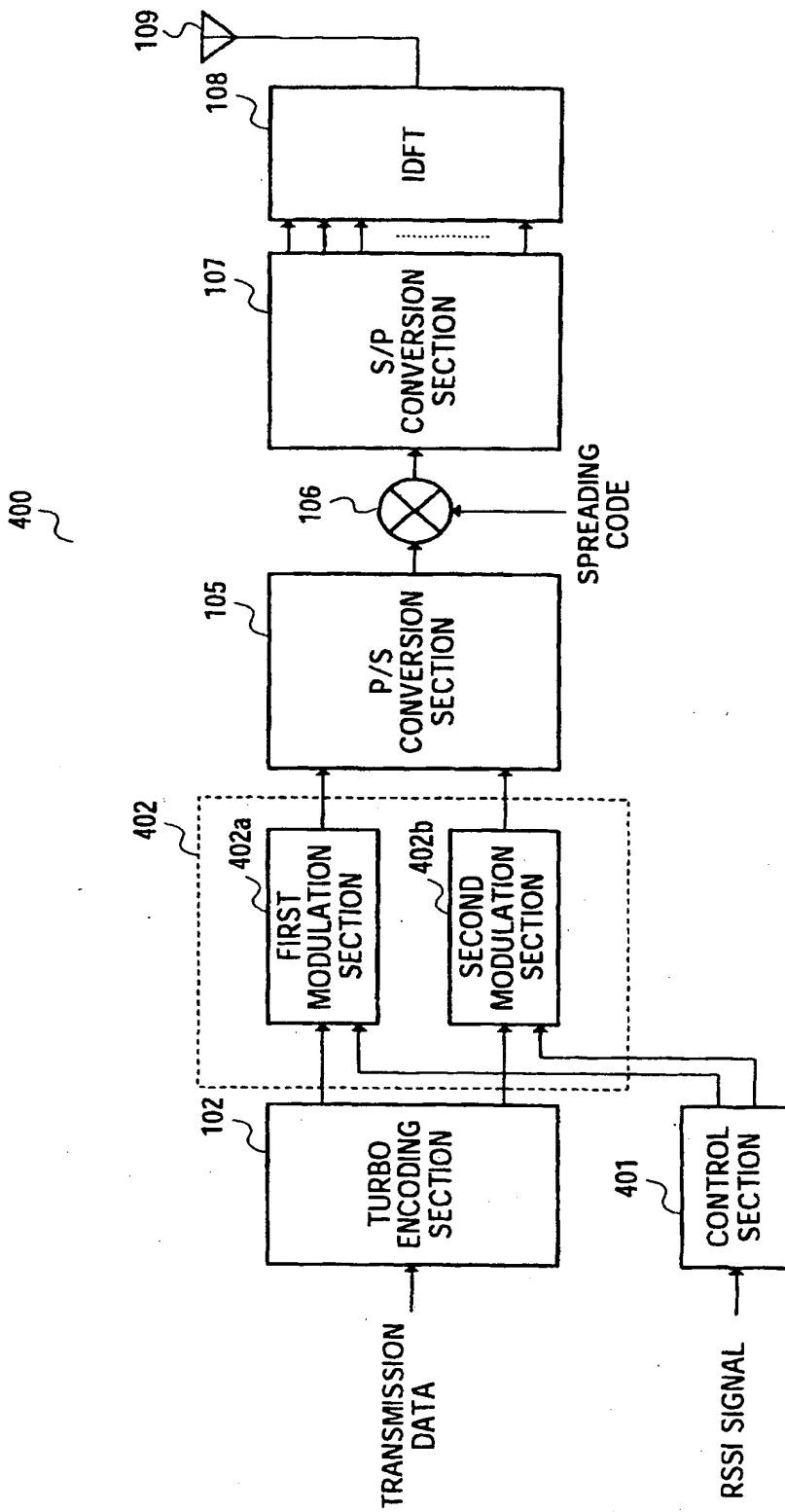


FIG.5

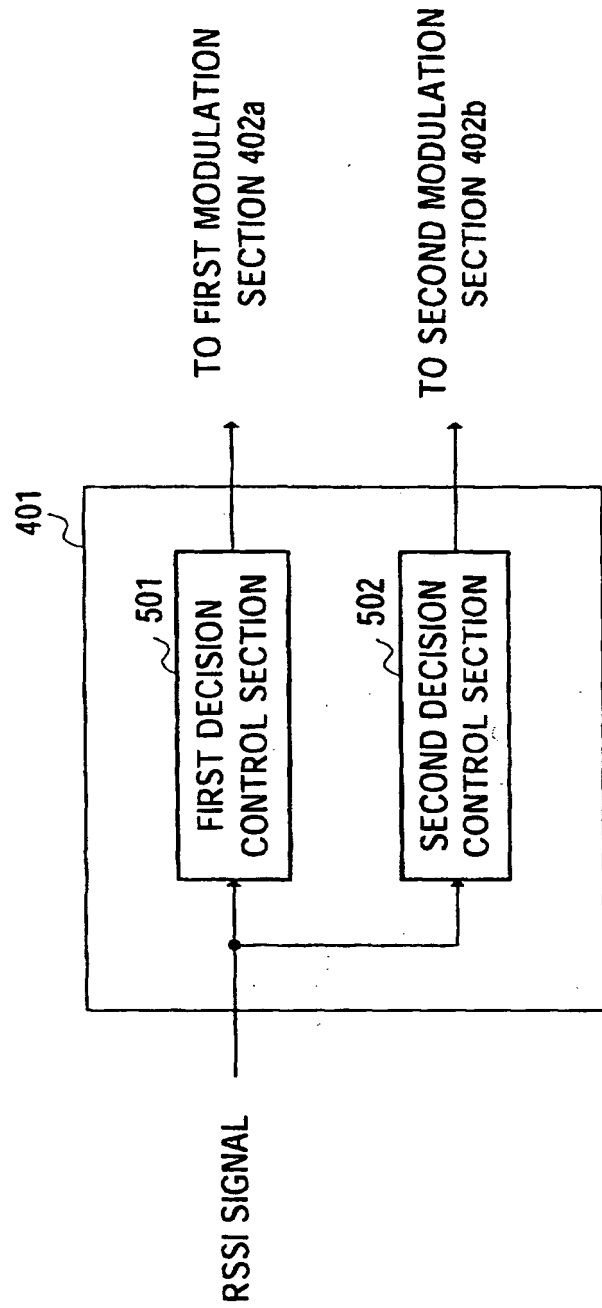


FIG.6

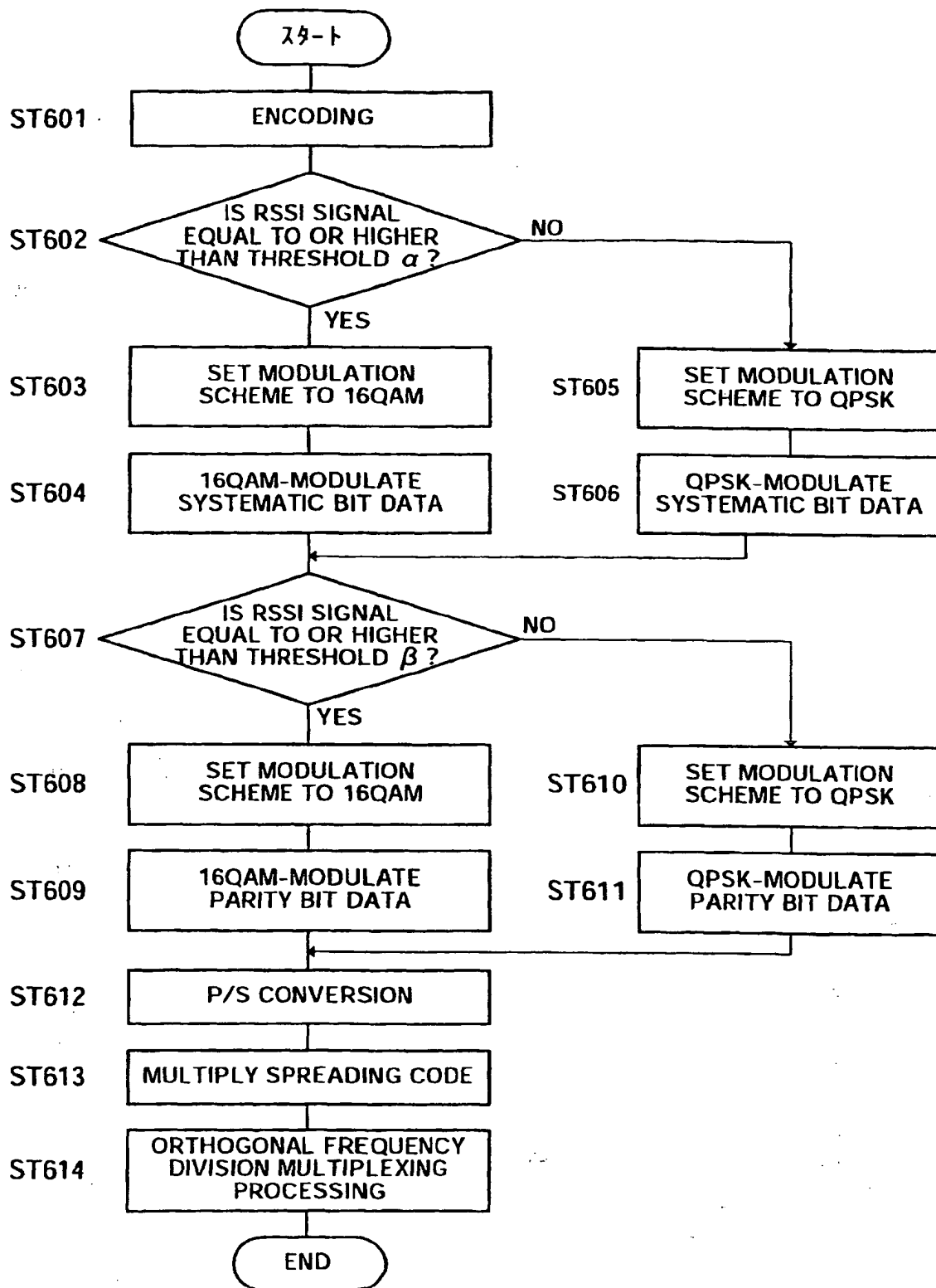


FIG.7

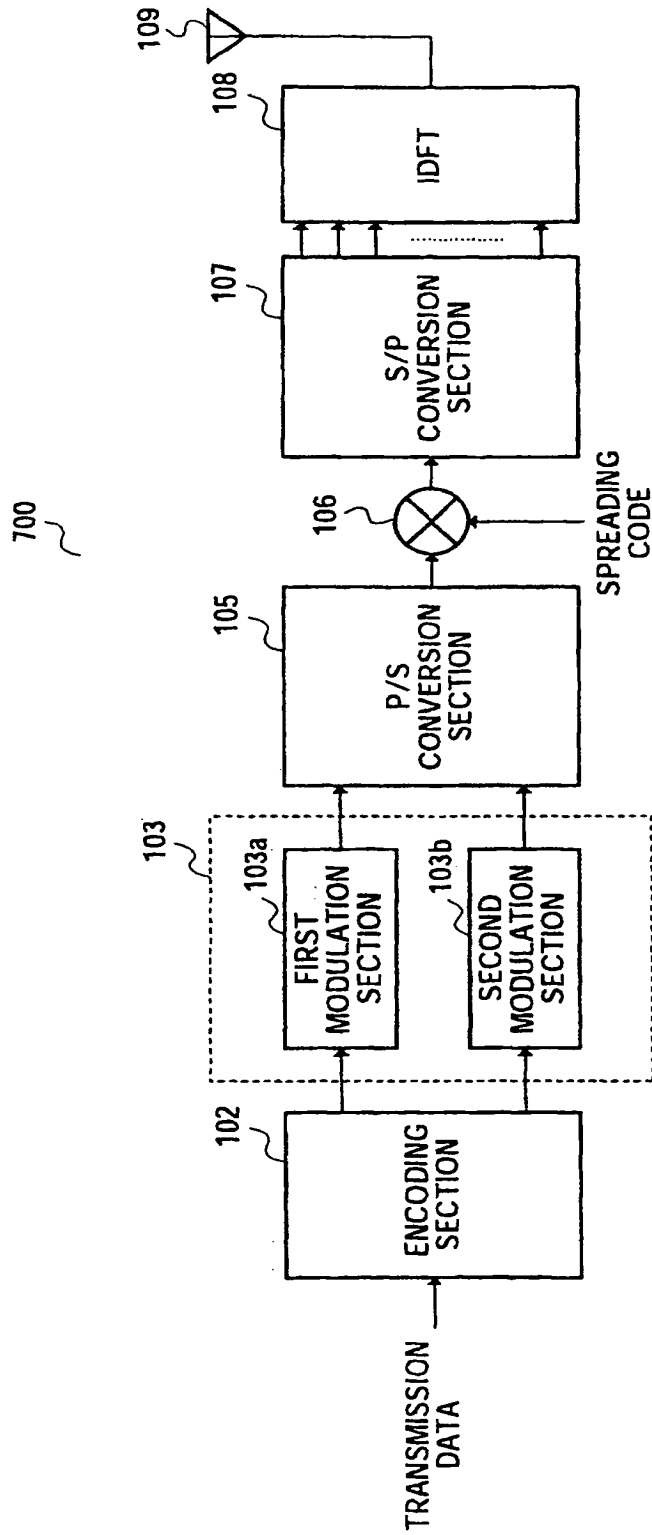


FIG.8

800 CDMA TRANSMISSION APPARATUS

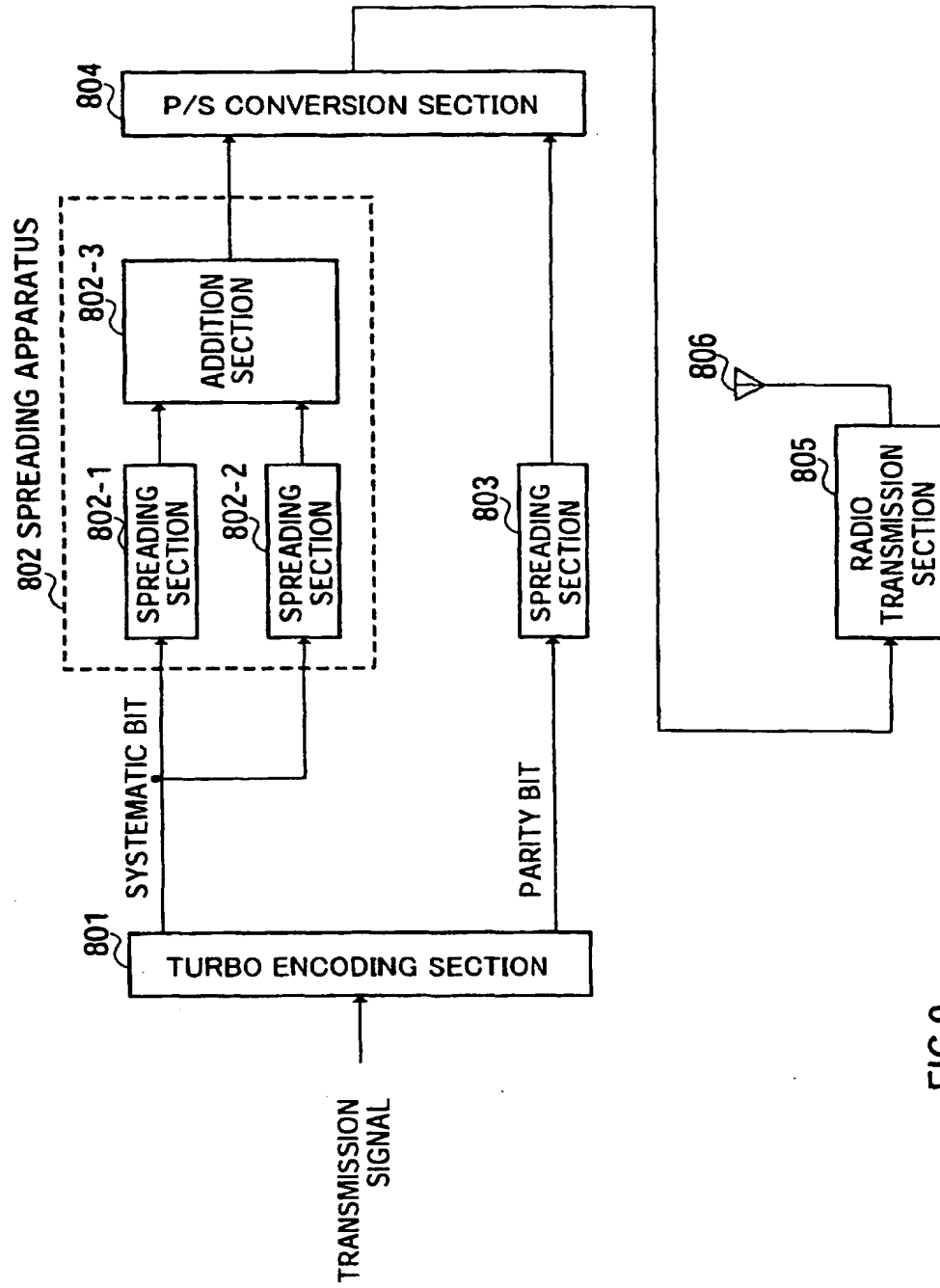


FIG.9

900 CDMA TRANSMISSION APPARATUS

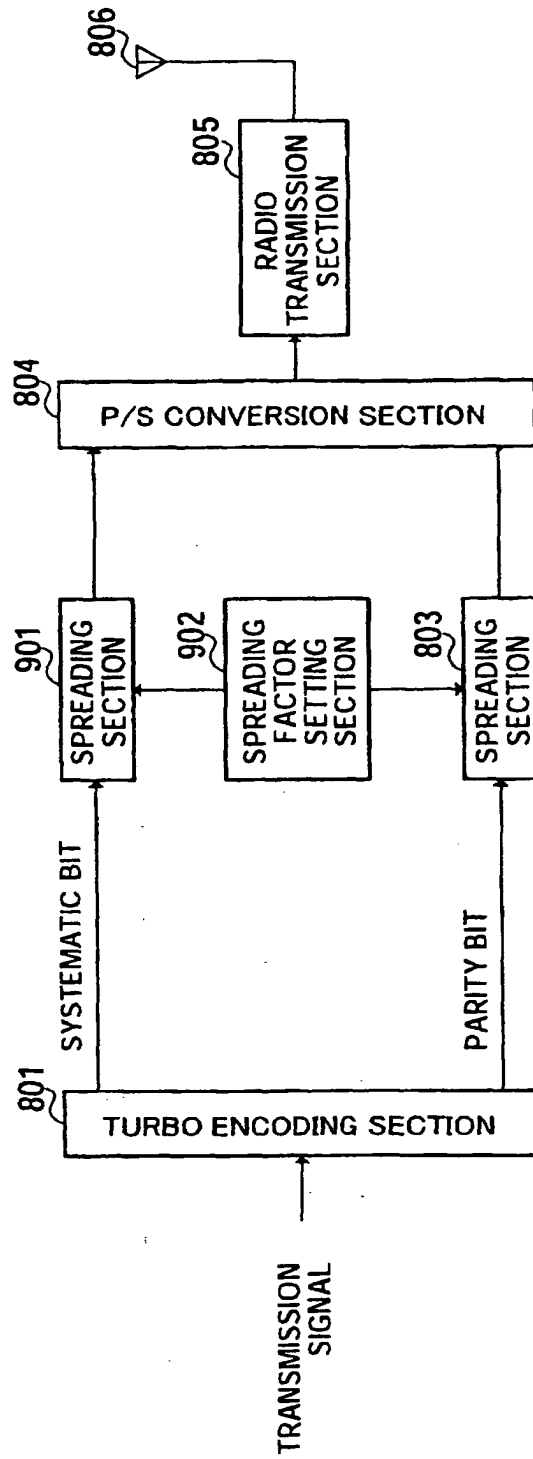


FIG.10

1000 CDMA TRANSMISSION APPARATUS

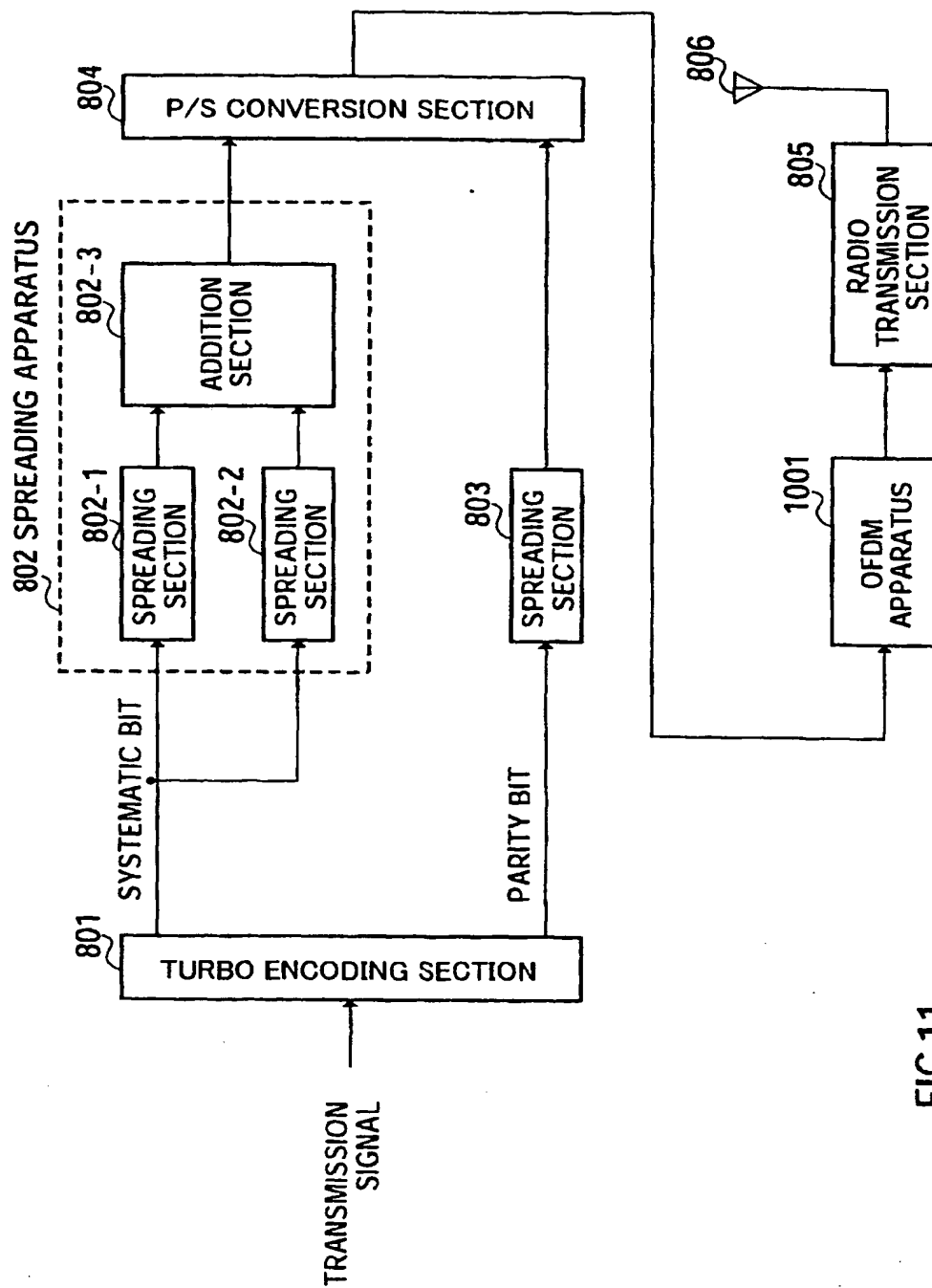


FIG.11

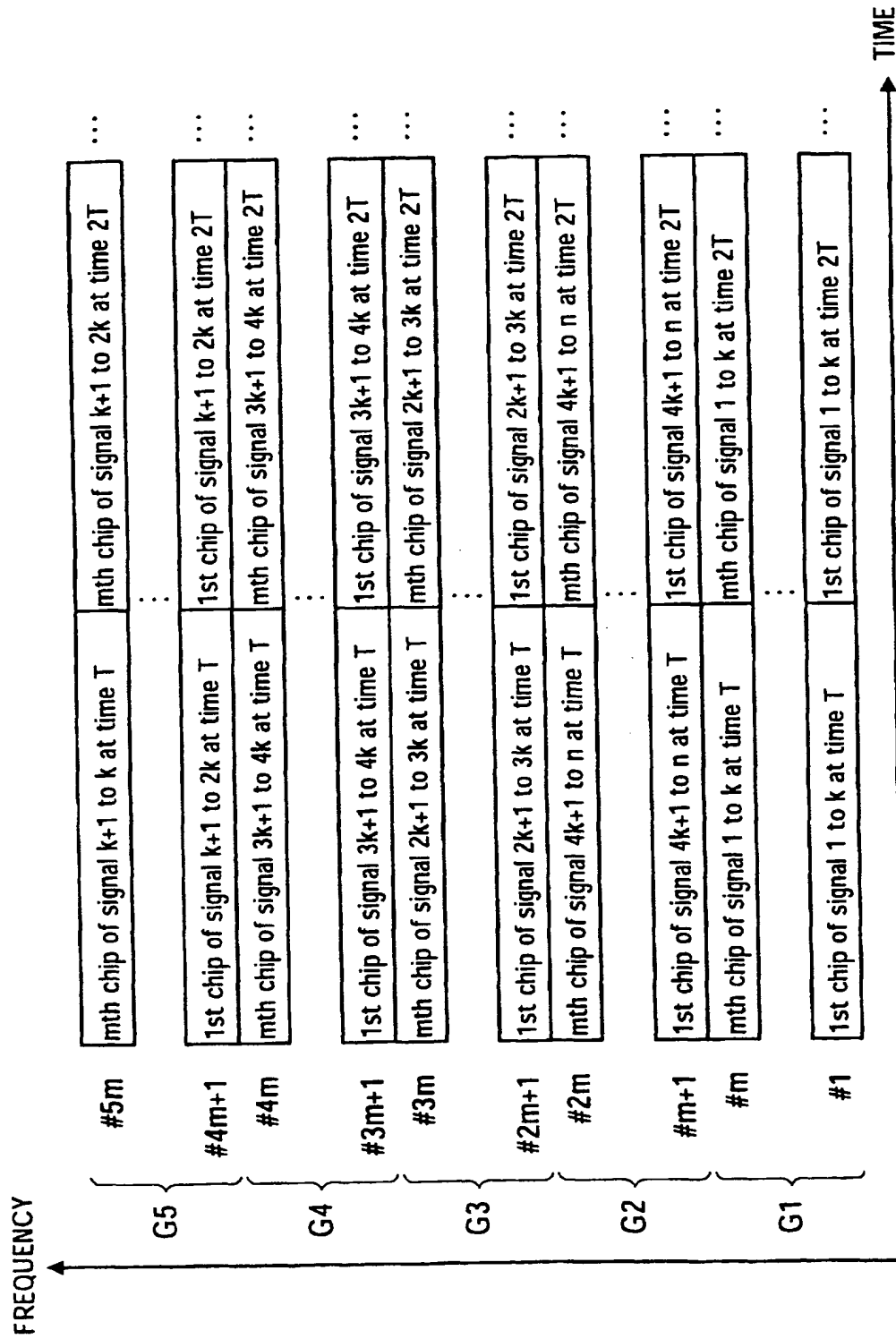


FIG. 12

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP03/13806

A. CLASSIFICATION OF SUBJECT MATTER Int.Cl. ⁷ H04L1/00, H04L27/00, H04L27/18		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int.Cl. ⁷ H04L1/00, H04L27/00, H04L27/18		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926-1996 Toroku Jitsuyo Shinan Koho 1994-2003 Kokai Jitsuyo Shinan Koho 1971-2003 Jitsuyo Shinan Toroku Koho 1996-2003		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP 2002-171298 A (Matsushita Electric Industrial Co., Ltd.), 14 June, 2002 (14.06.02), Par. Nos. [0004], [0005], [0009], [0013] & WO 02/25887 A1 & AU 200186257 A & BR 200107237 A & EP 1235404 A1 & US 2002/0163975 A1 & KR 2002068352 A & CZ 200201752 A3 & CA 1393089 A	1-11
Y	JP 11-330984 A (Toshiba Corp.), 30 November, 1999 (30.11.99), Par. Nos. [0035], [0323], [0329] & WO 98/58468 A1 & EP 0935363 A1 & JP 11-191758 A & JP 2001-007775 A & KR 2000068230 A & JP 3179060 B2 & JP 3349114 B2 & US 6490243 B1 & JP 3427039 B2	1-11
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "Z" document member of the same patent family		
Date of the actual completion of the international search 19 November, 2003 (19.11.03)		Date of mailing of the international search report 09 December, 2003 (09.12.03)
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer
Facsimile No.		Telephone No.

Form PCT/ISA/210 (second sheet) (July 1998)

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP03/13806

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X A	JP 2002-198938 A (Matsushita Electric Industrial Co., Ltd.), 12 July, 2002 (12.07.02), Fig. 2 & WO 02/54659 A1 & US 2003/0014709 A1 & KR 2002079913 A & CN 1406420 A & EP 1347593 A1	1, 9, 10 2-8, 11

Form PCT/ISA/210 (continuation of second sheet) (July 1998)